IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Fong PONG

U.S. Serial No.:	09/444,173	**Art Unit:**	2187
Filed:	November 19, 1999	**Examiner:**	Jasmine Song
For:	ASYNCHRONOUS CACHE COHERENCE ARCHITECTURE IN A SHARED MEMORY MULTIPROCESSOR WITH POINT-TO-POINT LINKS		

RESPONSE TO OFFICE ACTION

RECEIVED

Assistant Commissioner for Patents
Washington, D.C. 20231

JUL 0 2 2002

Sir:

Technology Center 2100

In response to the Office Action mailed March 13, 2002, Applicant respectfully requests the Examiner to consider the following remarks.

REMARKS

Reconsideration of this application is respectfully requested. Claims 1-20 are pending in this application.

35 U.S.C. § 103(a) Rejection

Claims 1-20 were rejected under 35 U.S.C. 103(a) as being allegedly unpatentable by Chang et al., U.S. Patent No. 6,119,204 ("Chang"). Applicant respectfully submits that Chang et al. does not teach or suggest the invention as claimed in claims 1, 9, and 19, and their dependent claims.

Chang does not disclose, teach or suggest the invention as claimed. Chang does not disclose, teach or suggest, for example:

A method for accessing memory in a multiprocessor system, the method comprising:

from a requesting processor, issuing a request for a block of data to one or more other processors and memory, each copy of the block of data